## IN THE DRAWINGS

Please find enclosed a copy of Figure 4 with a proposed amendment thereon indicated in red for

the approval of the Examiner.

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# Fig. 4

7: MODEL DEVELOPMENT HISTORY DATABASE

| ID INFORMATION | CIRCUIT FEATURE | | | | INSPECTION ITEM EXECUTION HISTORY | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|
| | TECHNOLOGY | CONDITION | MODEL NAME | PACKAGE | PHASE | DATA | NET LIST CHECK | PATTERN CHECK | SCAN CHECK | BOUNDARY SCAN CHECK | TIMING CHECK |
| AAA | CMOS9HD | cmos_3.3V | 6595 6E00 | TBG | ACCEPTED | 9/11 | ERROR 0 | ERROR 2 | NOT-EXECUTED | NOT-TARGETED | ERROR 0 |
| | | | | | RE-ACCEPTED | 9/14 | ERROR 0 | ERROR 0 | ERROR 0 | NOT-TARGETED | ERROR 0 |
| | | | | | BACK ANNOTATION | 9/18 | ERROR 0 | ERROR 0 | ERROR 0 | NOT-TARGETED | ERROR 0 |
| | CB7 | ttl_5V | 87543 | QOGN | ACCEPTED | 9/4 | EXCEPTED | ERROR 0 | NOT-TARGETED | ERROR 0 | ERROR 0 |
| | | | | | BACK ANNOTATION | 9/11 | ERROR 0 | ERROR 0 | NOT-TARGETED | ERROR 0 | ERROR 0 |
| ABC | CB8 | ttl_3.3V | 12345 | A8SI | ACCEPTED | 8/3 | ERROR 0 | ERROR 0 | ERROR 0 | NOT-TARGETED | ERROR 0 |
| | | | | | BACK ANNOTATION | 8/7 | ERROR 0 | ERROR 0 | ERROR 0 | NOT-TARGETED | ERROR 0 |
| | CMOS6 | cmos_5V | 6562 4E99 | QFP | ACCEPTED | 7/29 | ERROR 0 | ERROR 0 | NOT-TARGETED | NOT-TARGETED | ERROR 0 |
| | | | | | BACK ANNOTATION | 9/4 | ERROR 0 | ERROR 0 | NOT-TARGETED | NOT-TARGETED | ERROR 0 |
| | .. | .. | .. | .. | .. | .. | .. | .. | .. | .. | .. |